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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **YAMAGISHI, Yasuo, et al.**

Group Art Unit: 2829

Serial No.: **10/621,445**

Examiner: **Jermele M. HOLLINGTON**

Filed: **July 18, 2003**

P.T.O. Confirmation No.: 1112

For. **PROBE CARD AND TESTING METHOD OF SEMICONDUCTOR CHIP,  
CAPACITOR AND MANUFACTURING METHOD THEREOF**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: October 19, 2005

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Final Office Action dated **June 27, 2005** for one month, from **September 27, 2005** to **October 27, 2005**.

Attached is a check including the amount of \$910.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

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ARMSTRONG, KRATZ, QUINTOS,  
HANSON & BROOKS, LLP

*William L. Brooks*  
William L. Brooks  
Attorney for Applicant  
Reg. No. 34,129

WLB/ak  
Atty. Docket No. **030868**  
Suite 1000  
1725 K Street, N.W.  
Washington, D.C. 20006  
(202) 659-2930



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PATENT TRADEMARK OFFICE